

# Space-Time Trade-Off Optimization for a Class of Electronic Structure Calculations

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100 to 1000TB

$$S_{abij} = \sum_{cdefkl} A_{acik} \times B_{befl} \times C_{dfjk} \times D_{cdel}$$

$$S_{abij} = \sum_{ck} \left( \sum_{df} \left( \sum_{el} B_{befl} \times D_{cdel} \right) \times C_{dfjk} \right) \times A_{acik}$$

$$T1_{bcdfl} = \sum_{el} B_{befl} \times D_{cdel}$$

$$T2_{bcjfk} = \sum_{df} T1_{bcdfl} \times C_{dfjk}$$

$$S_{abij} = \sum_{ck} T2_{bcjfk} \times A_{acik}$$

(a) Formula sequence

```
T1=0; T2=0; S=0
for b, c, d, e, f, l
[ T1bcdfl += Bbefl Dcdel
for b, c, d, f, j, k
[ T2bcjfk += T1bcdfl Cdfjk
for a, b, c, i, j, k
[ Sabij += T2bcjfk Aacik
```






(b) Direct implementation  
(unfused code)

```
S = 0
for b, c
[ T1f = 0; T2f = 0
for d, f
[ for e, l
[ T1f += Bbefl Dcdel
for j, k
[ T2fjk += T1f Cdfjk
for a, i, j, k
[ Sabij += T2fjk Aacik
```

(c) Memory-reduced implementation (fused)

**Figure 1: Example illustrating use of loop fusion for memory reduction.**

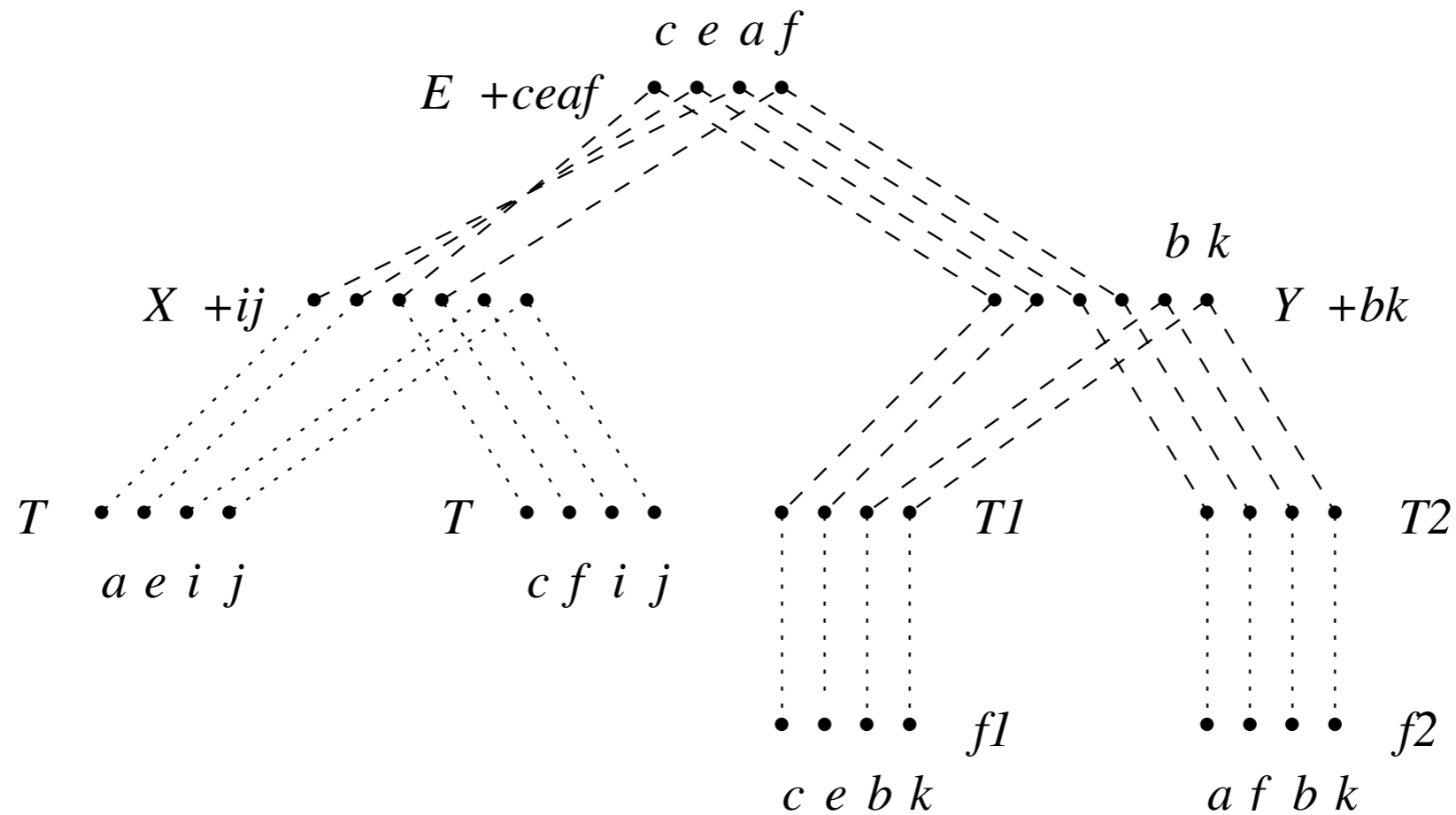
# Optimization System

-  Algebraic Transformations
-  Memory Minimization
-  Space-Time Transformation
-  Data Locality Optimization
-  Data Distribution and Partitioning

# Fusion Graph

can be used to facilitate enumeration of all possible compatible fusion configurations for a given computation tree.

The potential for fusion of a common loop among a producer-consumer pair of loop nests is indicated in the fusion graph through a dashed edge connecting the corresponding vertices.



**Figure 5: Fusion graph for unfused operation-minimal form of loop in Figure 2.**

# Example (I)

```

for a, e, c, f
[
  for i, j
  [ Xaecf += Tijae Tijcf
for a, f
[
  for c, e, b, k
  [ T1cebk = f1(c, e, b, k)
for c, e
[
  for a, f, b, k
  [ T2afbk = f2(a, f, b, k)
for c, e, a, f
[
  for b, k
  [ Yceaf += T1cebk T2afbk
for c, e, a, f
[
  E += Xaecf Yceaf

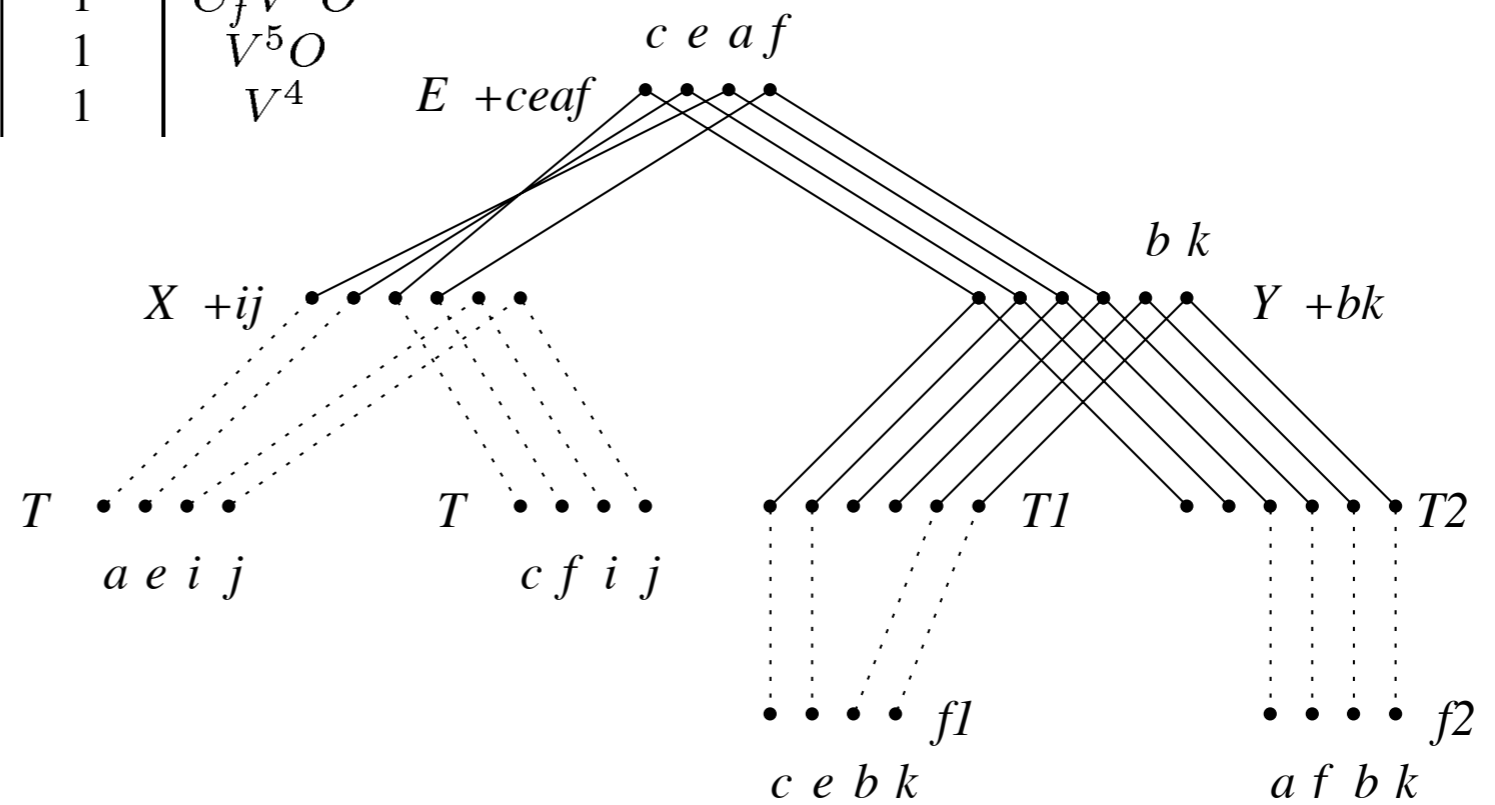
```

```

⇒
for a, e, c, f
[
  for i, j
  [ X += Tijae Tijcf
  for b, k
  [
    T1 = f1(c, e, b, k)
    T2 = f2(a, f, b, k)
    Y += T1 T2
  E += X Y

```

array	space	time
X	1	$V^4 O^2$
T1	1	$C_f V^5 O$
T2	1	$C_f V^5 O$
Y	1	$V^5 O$
E	1	$V^4$



(a) Fully fused computation from Fig. 3.

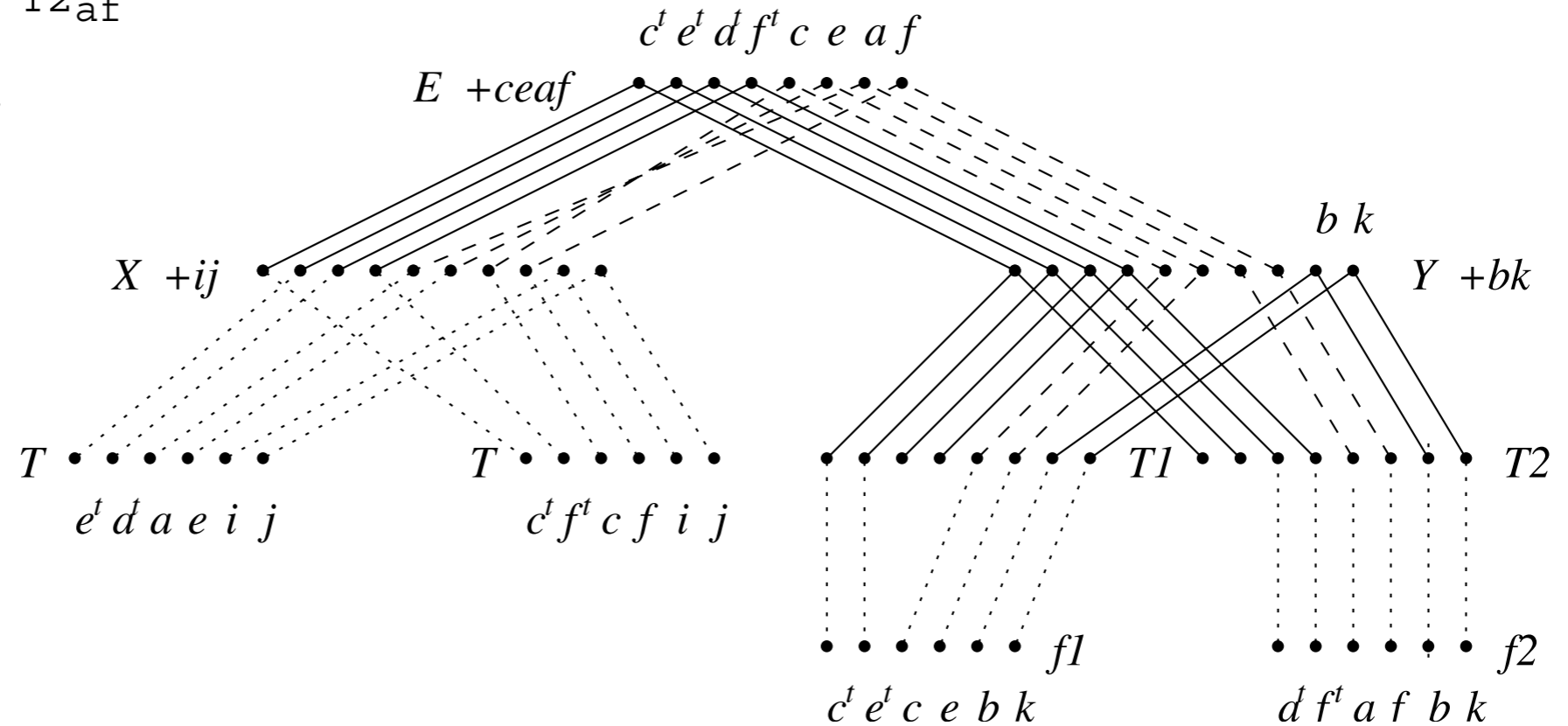
# Example (2)

```

for at, et, ct, ft
[
  for a, e, c, f
  [
    for i, j
    [
      Xaecf += Tijae Tijcf
    ]
  ]
  for b, k
  [
    for c, e
    [
      T1ce = f1(c, e, b, k)
    ]
    for a, f
    [
      T2af = f2(a, f, b, k)
    ]
    for c, e, a, f
    [
      Yceaf += T1ce T2af
    ]
  ]
  for c, e, a, f
  [
    E += Xaecf Yceaf
  ]
]

```

array	space	time
X	$B^4$	$V^4 O^2$
T1	$B^2$	$C_f V^5 O / B^2$
T2	$B^2$	$C_f V^5 O / B^2$
Y	$B^4$	$V^5 O$
E	1	$V^4$



(b) Partially fused computation from Fig. 4.

# Space-Time Tradeoff Exploration

- Search among all possible ways of introducing redundant loop indices in the fusion graph to reduce memory requirements, and determine the optimal set of lower dimensional intermediate arrays for various total memory limits. In this step, the use of tiling for partial reduction of array extents is not considered. However, among all possible combinations of lower dimensional arrays for intermediates, the combination that minimizes recomputation cost is determined, for a specified memory limit. The range from zero to the actual memory limit is split into subranges within which the optimal combination of lower dimensional arrays remains the same.
- Because the first step only considers complete fusion of loops, each array dimension is either fully eliminated or left intact, i.e. partial reduction of array extents is not performed. The objective of the second step is to allow for such arrays. Starting from each of the optimal combinations of lower dimensional intermediate arrays derived in the first step, possible ways of using tiling to partially expand arrays along previously compressed dimensions are explored. The goal is to further reduce recomputation cost by partially expanding arrays to fully utilize the available memory



# Space-Time optimization

- 🔊 Dimension Reduction for Intermediate Arrays
  - 🔊 search among all possible combination
  - 🔊 memory and recomputation costs
- 🔊 Partial Expansion of Reduced Intermediates
  - 🔊 resort to array expansion
  - 🔊 for determining the best choice for array expansion costs

# Result

